

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In Re Application Of	)	
	)	Group Art Unit: 2827
Ken Takeuchi et al.	)	
	)	Examiner: H. Ho
Serial No.: 10/073,999	)	
	)	
Filed: February 14, 2002	)	Confirmation No. 9741
	)	
For: Nonvolatile Semiconductor Memory	)	Atty. Dkt. No. 001701.00140
Having Plural Data Storage Portions	)	
For A Bit Line Connected To Memory	)	
Cells	)	

U.S. Patent and Trademark Office  
Mail Stop **Non-Fee Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**Amendment**

The present paper is responsive to the non-final Office Action dated November 10, 2005. Applicants request entry of the following remarks into the file of the above-identified application. If any fees are required for this paper, please charge such fees to deposit account no. 19-0733.

**Listing of Claims** begins on page 2 of this paper.

**Remarks** begin on page 8 of this paper.